



Specifications

Discrete Ceramic Filter – Dual Mode

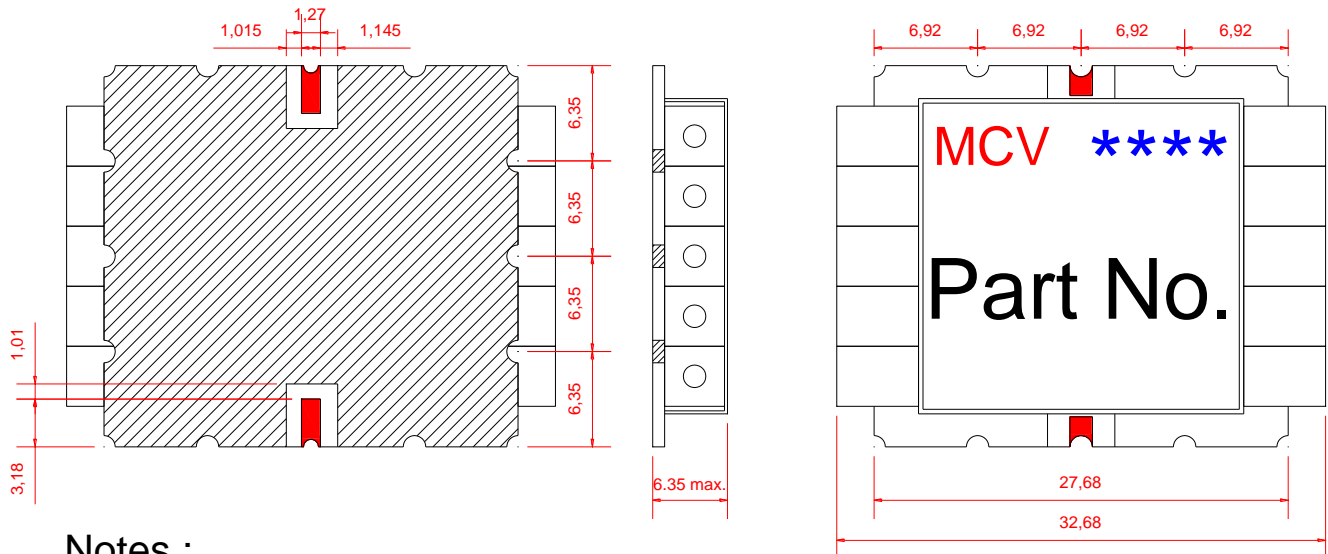
Design	Engineering	Approval
CYK	MHJ	ECL

Customer:	Date: 04-04-12	P/N: DPX1800-2305-100A1
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Electrical Specifications

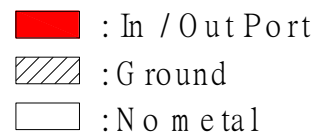
No.	Parameter	Unit	Specification	
1	Center Frequency	MHz	1800	2305
2	Pass Bandwidth	MHz	$F_c \pm 50$ (1750 ~ 1850MHz)	$F_c \pm 105$ (2195 ~ 2405MHz)
3	Insertion Loss at F_c	dB	1.0 max.	
4	Ripple in BW	dB	0.5 max.	
5	Return Loss in BW	dB	15.0 min.	
6	Attenuation	dB	25 min. @ 1550MHz	
			20 min. @ 2040 ~ 2055MHz	
			25 min. @ 2750MHz	
7	Input Power	Watt	3 max.	
8	IN/OUT Impedance	Ω	50	
9	Operating Temp. Range	$^{\circ}\text{C}$	-40 ~ +85	

Dimensions [Unit: mm]



Notes :

1. Surface mount package
2. Dimensions in millimeters
3. Max reflow temperature 240 $^{\circ}\text{C}$ for 10 sec
4. If boards are cleaned after installation units must be completely dried
5. Tolerance unless specified : ± 0.3
6. **** -> Date code (YYWW)



Simulation Graph

