



Specification

Cavity Quadruplexer

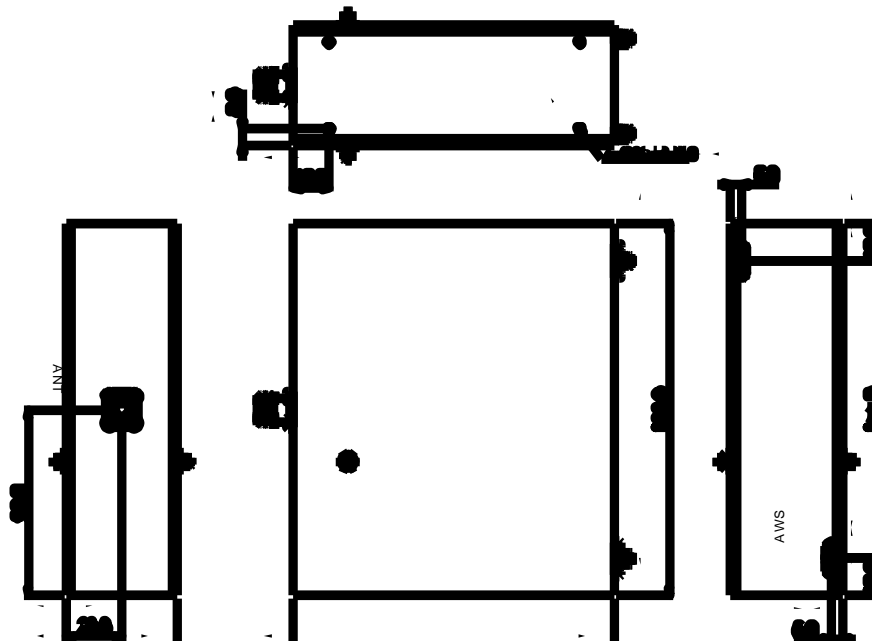
Design	R & D	Approval
CYK	YHKW	EL

Customer:	Date: 3/18/10	P/N: MCC4-000002A2
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Electrical Specification

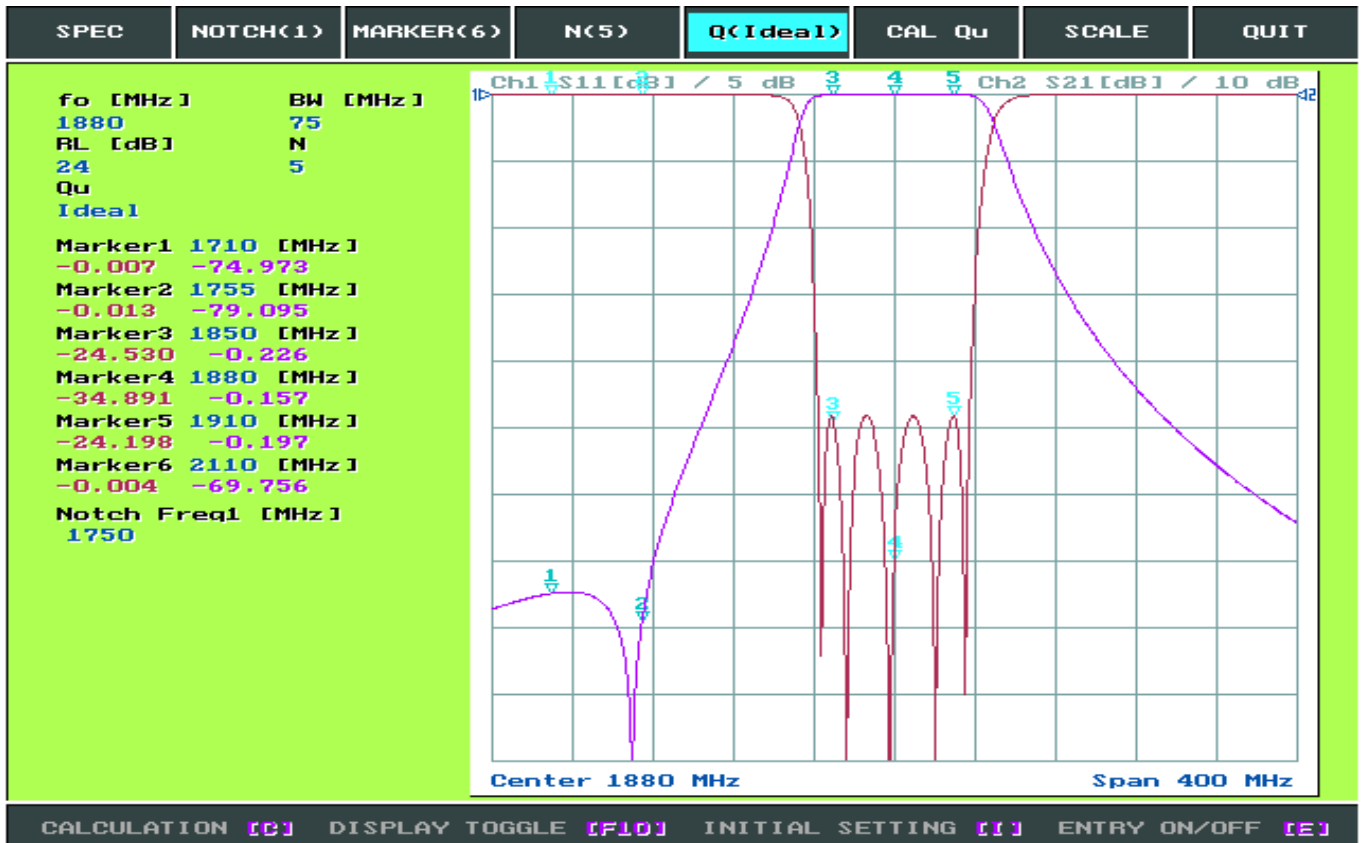
No.	Parameter	Unit	Specification			
			PCS Path		AWS Path	
			LOW(Uplink)	HIGH(Downlink)	LOW(Uplink)	HIGH(Downlink)
1	Center Frequency	MHz	1880	1960	1732.5	2132.5
2	Frequency Range	MHz	1850~1910	1930~1990	1710~1755	2110~2155
3	Insertion Loss in BW	dB	0.5 max.			
4	Passband Ripple	dB	±0.25 max.			
5	Return Loss in BW	dB	20.0 min.			
6	Differential Group Delay (in any 3.84MHz Band)	ns	10 max.			
7	Attenuation	dB	65 min. @ 1710 ~ 1755MHz 65 min. @ 2110 ~ 2155MHz 75 min. @ 2400 ~ 2483MHz 75 min. @ 5160 ~ 5845MHz 35 min. @ 5845 ~ 11800MHz		65 min. @ 1850 ~ 1910MHz 75 min. @ 2400 ~ 2483MHz 75 min. @ 5160 ~ 5845MHz 35 min. @ 5845 ~ 11800MHz	
8	Isolation	dB	65 min. @ AWS Downlink		65 min. @ PCS Path	
9	Power Capability	dBm	46 max.			
10	IN/OUT Impedance	Ω	50			
11	Operating Temp. Range	°C	-20 ~ +85			
12	Connector	-	ANT-N(Female), PCS, AWS Path-SMA(Female)			
13	Finish	-	Black Epoxy Paint			

Dimension (Unit: mm)

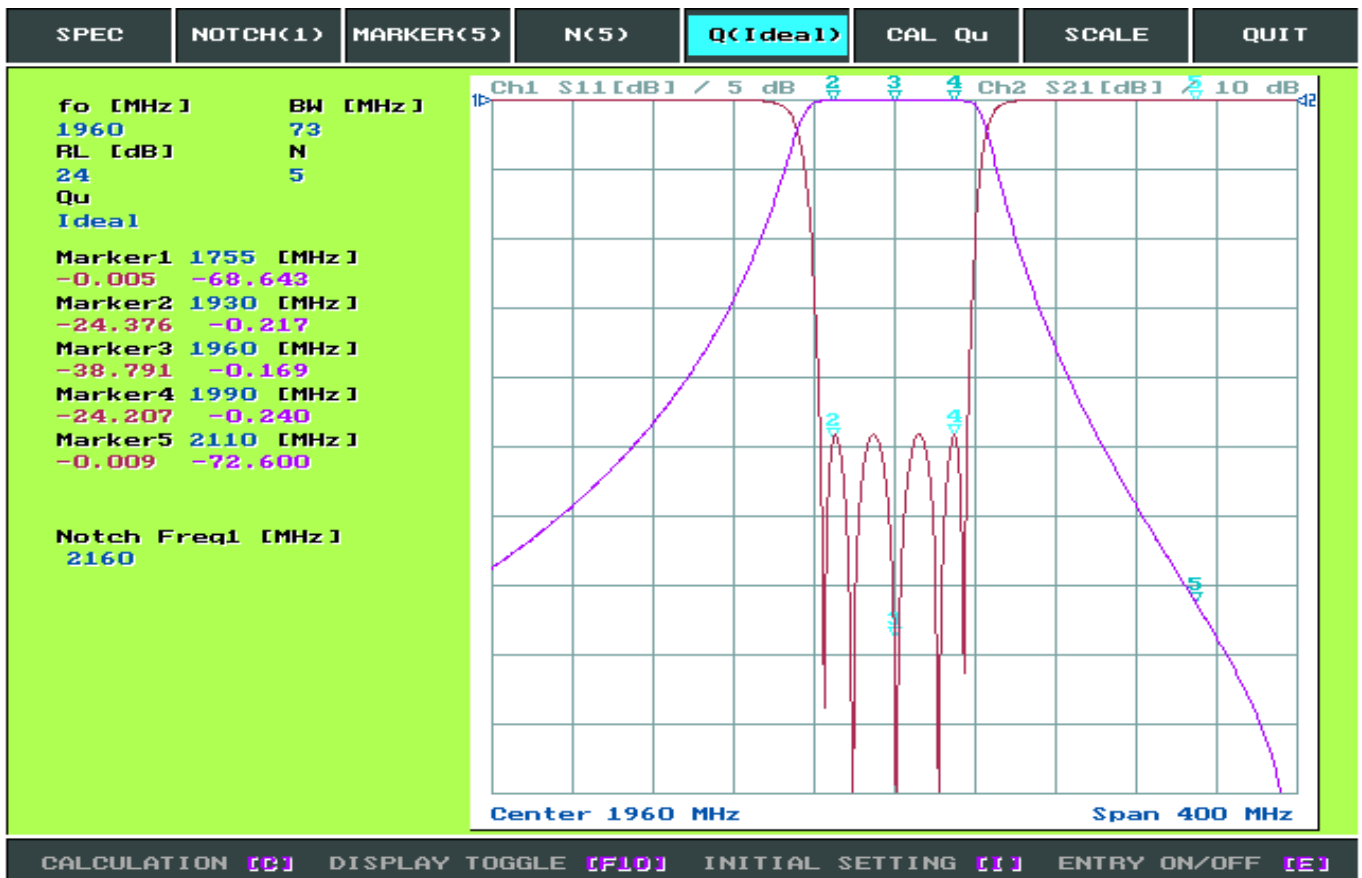


Simulation Graphs

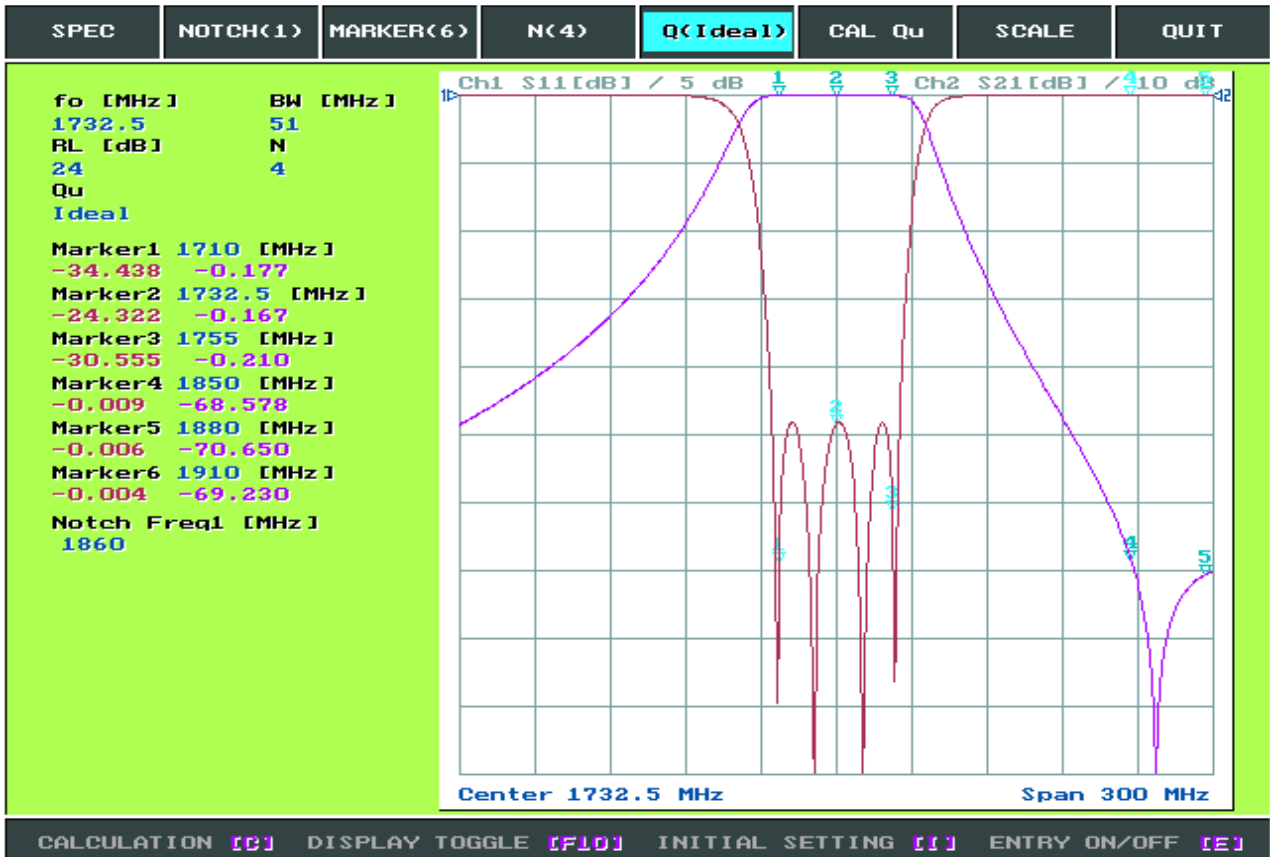
1. PCS UL



2. PCS DL



3. AWS UL



4. AWS DL

